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Transmission Date:

December 13, 2004

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Buerke, et al.

Docket No.:

INF-118

Serial No:

10/724,136

Art Unit:

2811

Date Filed:

December 1, 2003

Title:

Low Resistance Barrier for a Microelectronic Component and Method for

Fabricating the Same

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Respectfully submitted,

Julié Russell

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Examiner:

Vu, Hung K.

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Low Resistance Barrier for a Microelectronic Component and Method for

Fabricating the Same

Mail Stop: Amendment Commissioner for Patents

P. O. Box 1450

Alexandria, VA 22313-1450

AMENDMENT

Dear Sir:

The following amendments and remarks are presented in response to the Examiner's Office Action mailed September 24, 2000. Please amend the above-referenced application as follows.